

76.00mm (3.0 inch), Pure Green LED Numeric Display Single Digit 7-segment LED Display

Technical Data Sheet

Features

- High reliability
- Low power consumption
- Excellent characters appearance
- Evenly lighted segments
- Wide viewing angle
- Easy mounting on PCB or sockets
- I.C. compatible
- RoHS compliant

Descriptions

- The KW1-3003APGB is a 76.00mm (3.0 inch) digit height seven-segment LED display.
- The display provides excellent reliability in bright ambient light.
- The device is as either common anode or common cathode.
- The device is made with white diffused segments and black surface.

Applications

- Instrument panels
- Display time and digital combination
- Test and measurement equipment
- Industrial and instrumental applications
- Timer, controller, DPM, counter, etc.
- Control units

Device Selection Guide

Part No.	Emitting Color	Circuit Common		
KW1-3003APGB	Pure Green	Common Anode		

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Issue No.: G-001-Rev-3
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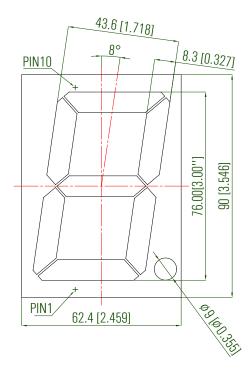
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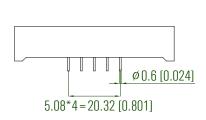
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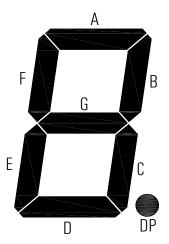
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Package Dimension









Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is \pm 0.25 mm (.010") unless otherwise noted.
- 3. The specifications, characteristics and technical data described in the datasheet are subject to change without prior notice.

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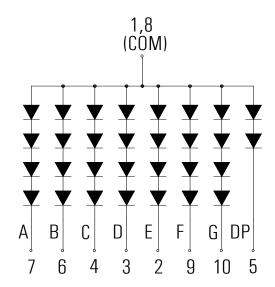
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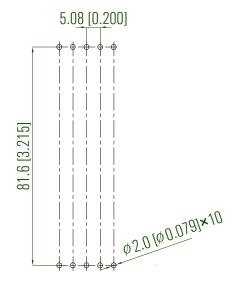
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Internal Circuit Diagram:



Recommended PCB Layout:



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Absolute Maximum Ratings at Ta=25°C

Parameters	Symbol	Max	Unit	
Power Dissipation (Per chip)	P_d	64	mW	
Peak Forward Current (Per segment) Peak Forward Current (DP) (1/10 Duty Cycle, 0.1ms pulse width)	I _{FP}	40 40	mA	
DC Forward Current (Per segment) DC Forward Current (DP)	I _F	20 20	mA	
Reverse Voltage (Per chip)	V_R	5	V	
Operating Temperature Range	T_{opr}	-40°C to +80°C		
Storage Temperature Range	T_{stg}	-40°C to +85°C		
Soldering Temperature	T _{sld}	260°C for 5 Seconds		

Electrical Optical Characteristics at Ta=25°C

Parameters	Symbol	Min.	Тур.	Max.	Unit	Test Condition
Average Luminous Intensity	lv -	68.0	136.0		mcd	IF=10mA (Note a)
		136.0	272.0		mcd	IF=20mA (Note a)
Luminous Intensity Matching Ratio	l _{v-m}			2:1		IF=20mA
Peak Emission Wavelength	λр		520		nm	IF=20mA
Dominant Wavelength	λd		525		nm	IF=20mA (Note b)
Spectral Line Half-Width	Δλ		20		nm	IF=20mA
Forward Voltage (Per segment)	V		11.6	12.4	V	IF=20mA (Note c)
Forward Voltage (DP)	V_{F}		5.8	6.4	V	ir-zoma (Note c)
Reverse Current Per Segment	I_R			50	μΑ	VR=5V

Notes:

- a. Luminous intensity is measured with a light sensor and filter combination that approximates the CIE eye-response curve. Tolerance of Luminous Intensity: \pm 10 %
- b. The dominant wavelength (λd) is derived from the CIE chromaticity diagram and represents the single wavelength which defines the color of the device.
- c. Tolerance of Forward Voltage: ± 0.1V

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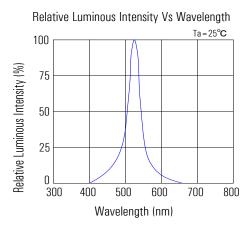
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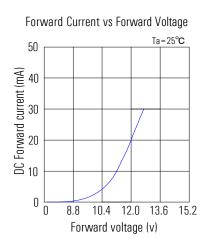
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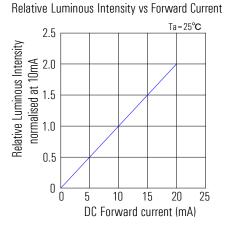
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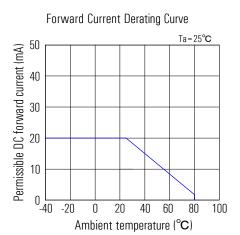
Typical Electrical/Optical Characteristics Curves



Per Segment







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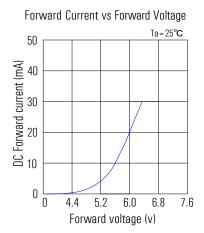
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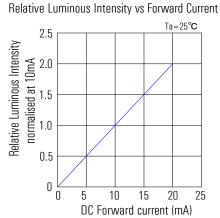


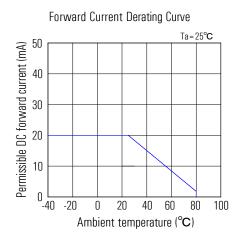
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Decimal Point (DP)







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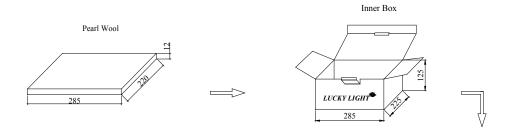
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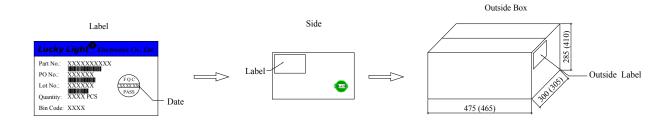


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Packing & Label Specifications:





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- f. Over-current-proof

Customer must apply resistors for protection, otherwise slight voltage shift will cause big current change (Burn out will happen).

- g. Storage
 - 1. Before opening the package, the LEDs should be kept at 30°Cor less and 80%RH or less.
 - 2. The LEDs should be used within a year.
 - 3. After opening the package, the LEDs should be kept at 30°Cor less and 60%RH or less.
- h. ESD (Electrostatic Discharge)

Static Electricity or power surge will damage the LED. Suggestions to prevent ESD damage:

IUse of a conductive wrist band or anti-electrostatic glove when handling these LEDs.

IAII devices, equipment, and machinery must be properly grounded.

IWork tables, storage racks, etc. should be properly grounded.

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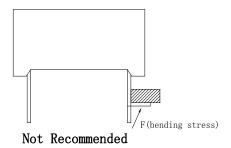
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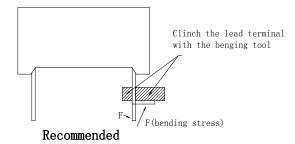
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Through Hole Display Mounting Method

Lead Forming:

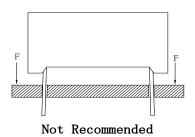
- 1. Do not bend the component leads by hand without proper tools.
- 2. The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.

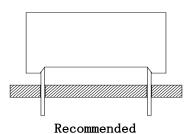




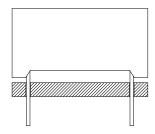
Installation:

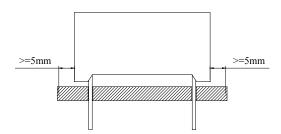
- 1. The installation process should not apply stress to the lead terminals.
- 2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals.





3. The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering.





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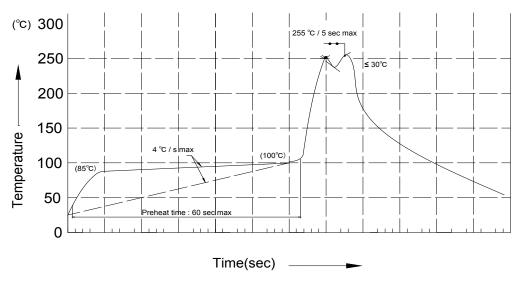
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Recommended Wave Soldering Profiles



Notes:

- 1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C.
- 2. Peak wave soldering temperature between $245^{\circ}\text{C} \sim 255^{\circ}\text{C}$ for $3 \sec (5 \sec \text{max})$.
- 3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
- 4. Fixtures should not incur stress on the component when mounting and during soldering process.
- 5. SAC 305 solder alloy is recommended.
- 6. No more than one wave soldering pass.
- 7. During wave soldering, the PCB top-surface temperature should be kept below 105°C.

Soldering General Notes:

- 1. Through-hole displays are incompatible with reflow soldering.
- 2. If components will undergo multiple soldering processes, or other processes where the omponents may be subjected to intense heat, please check with luckylight for compatibility.

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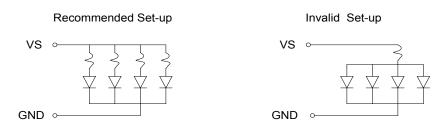
Cleaning:

- 1. Mild "no-clean" fluxes are recommended for use in soldering.
- 2. If cleaning is required, luckylight recommends to wash components with water only.

 Do not use harsh organic solvents for cleaning because they may damage the plastic parts.
- 3. The cleaning process should take place at room temperature and the devices should not be washed for more than one minute.
- 4. When water is used in the cleaning process, immediately remove excess moisture from the component with forced-air drying afterwards.

Circuit Design Notes:

- 1. Protective current-limiting resistors may be necessary to operate the LEDs within the specified range.
- 2. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor.



- 3. The driving circuit should be designed to protect the LED against reverse voltages and transient voltage spikes when the circuit is powered up or shut down.
- 4. The safe operating current should be chosen after considering the maximum ambient temperature of the operating environment.
- 5. Prolonged reverse bias should be avoided, as it could cause metal migration, leading to an increase in leakage current or causing a short circuit.