

# 0.20 inch (5.08mm), Yellow Single Digit 7-segment Surface Mount LED Display

#### **Technical Data Sheet**

#### **Features**

- 0.20inch (5.08mm) digit height.
- The thickness is thinness than tradition display.
- Packaged in tape and reel for SMT manufacturing.
- Low current operation.
- Excellent characters appearance.
- Categorized for luminous intensity.
- Available in CA and CC.
- The product itself will remain within RoHS compliant Version.



#### **Descriptions**

- The KW1-S201AYA/ KW1-S201CYA is a 0.20inch (5.08mm) height single digit display.
- The display provides excellent reliability in bright ambient light.
- The device is made with white segments and gray surface.

# **Applications**

- Home and smart appliances
- Display time and digital combination
- Industrial and instrumental applications
- Numeric status

#### **Device Selection Guide**

Part No.	Emitting Color	Polarity
KW1-S201AYA	Yellow	Common Anode
KW1-S201CYA	Yellow	Common Cathode

Spec No.: KW1-S201 Date: 16-Mar-2017 Issue No.: G-001-Rev-3 E-mail: sales@luckylight.cn Luckylight Electronics Co., Ltd https:// www.luckylight.cn Page: 1/9

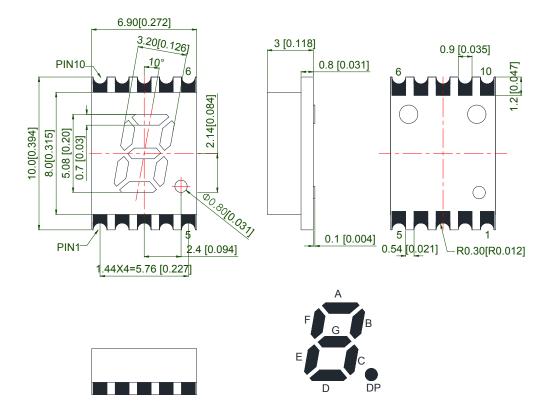
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## **Technical Data Sheet**

## **Package Dimension**



#### Notes:

- 1. All dimensions are in millimeters (inches).
- 2. Tolerance is  $\pm$  0.25 mm (.010") unless otherwise noted.
- 3. The gap between the reflector and PCB shall not exceed 0.25mm.

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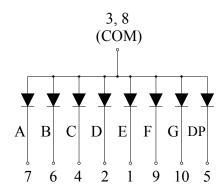
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## **Technical Data Sheet**

## **Internal Circuit Diagram:**

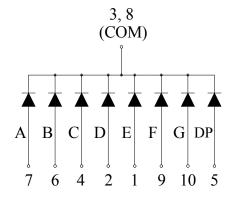
## **Internal Circuit Diagram (Common Anode)**

KW1-S201AYA



## **Internal Circuit Diagram (Common Cathode)**

KW1-S201CYA



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#### **Technical Data Sheet**

# Absolute Maximum Ratings at Ta=25°C

Parameters	Symbol	Max	Unit
Power Dissipation Per Segment	$P_{d}$	48	mW
Peak Forward Current Per Segment (1/10 Duty Cycle, 0.1ms Pulse Width)	I <sub>FP</sub>	100	mA
Forward Current Per Segment	I <sub>F</sub>	20	mA
Reverse Voltage Per Segment	$V_R$	5	V
Operating Temperature Range	$T_{opr}$	-40°C to +1	.00°C
Storage Temperature Range	$T_{stg}$	-40°C to +1	.05°C
Soldering Temperature	T <sub>sld</sub>	260°C for 5 Seconds	

# Electrical Optical Characteristics at Ta=25°C

Parameters	Symbol	Min.	Тур.	Max.	Unit	Test Condition
Average Luminous Intensity	lv	7.0	14.0		mcd	IF=10mA (Note a)
Luminous Intensity Matching Ratio	I <sub>v-m</sub>			2:1		IF=10mA
Peak Emission Wavelength	λр		593		nm	IF=20mA
Dominant Wavelength	λd		590		nm	IF=20mA (Note b)
Spectral Line Half-Width	Δλ		20		nm	IF=20mA
Forward Voltage Per Segment	V <sub>F</sub>		2.0	2.4	V	IF=20mA
Reverse Current Per Segment	$I_R$			50	μΑ	VR=5V

#### Notes:

- a. Luminous intensity is measured with a light sensor and filter combination that approximates the CIE eye-response curve.
- b. The dominant wavelength ( $\lambda d$ ) is derived from the CIE chromaticity diagram and represents the single wavelength which defines the color of the device.

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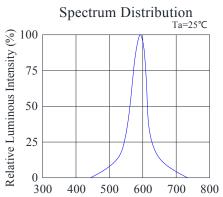
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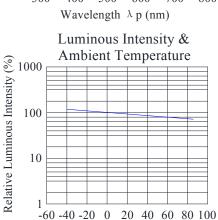


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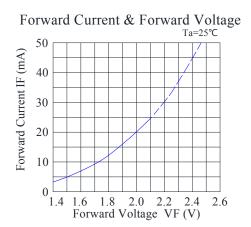
## **Technical Data Sheet**

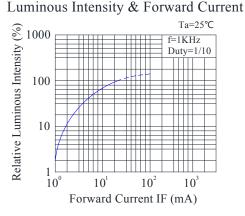
# Typical Electrical / Optical Characteristics Curves (25°C Ambient Temperature Unless Otherwise Noted)

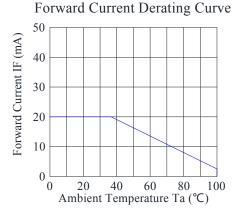




Ambient Temperature Ta (°C)







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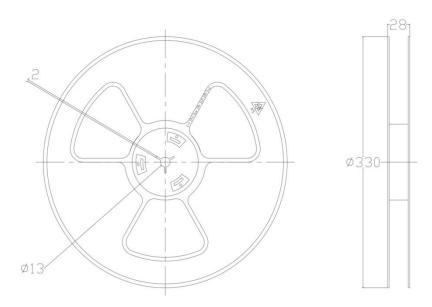
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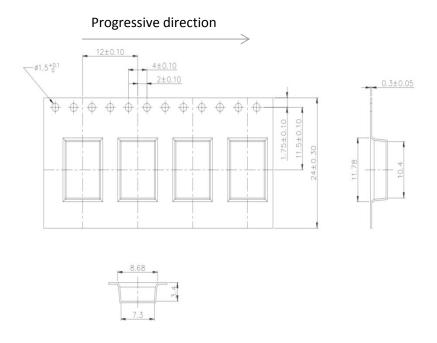
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## Reel Dimensions (units: mm)



# **Carrier Tape Dimensions** (units: mm)



Note: Tolerances unless mentioned ±0.25mm.

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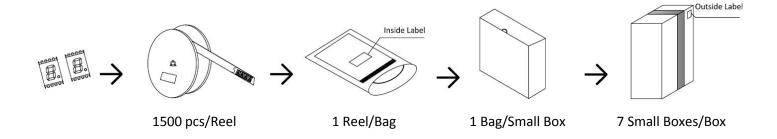
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#### **Technical Data Sheet**

## **Packing & Label Specifications**

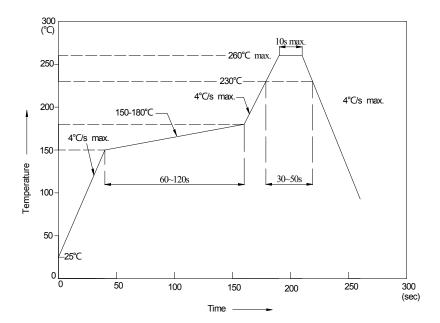


#### **Precautions for Use**

#### 1. Caution in ESD

Static electricity and surge damages the LED. It is recommended to use a wrist band or anti-electrostatic glove when handling the LED. All devices equipment and machinery must be properly grounded.

#### 2. SMT Soldering Condition



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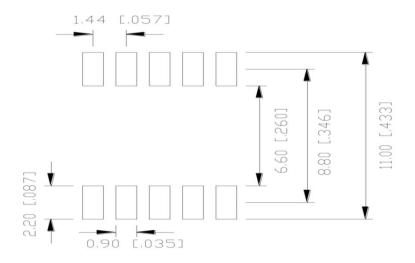


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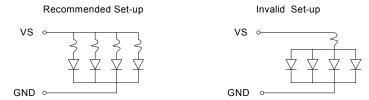
Reflow Soldering (Two times only)		Soldering	Soldering Iron (One time only)		
Pre-heat	120~150°C	Temperature	300°C Max		
Pre-heat time	120 sec. Max.	Soldering time	3 sec. Max.		
Peak temperature	260°C Max.				
Soldering time	5 sec. Max.				

#### 3. Recommended Soldering Pattern



#### 4. Circuit Design Notes:

- 1. Protective current-limiting resistors may be necessary to operate the LEDs within the specified range.
- 2. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor.



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- 6. The LEDs should be operated with forward bias. The driving circuit must be designed so that the LEDs are not subjected to forward or reverse voltage while it is off. If reverse voltage is continuously applied to the LEDs, it may cause migration resulting in LED damage.

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