

KWM-20571AGA/KWM-20571CGA

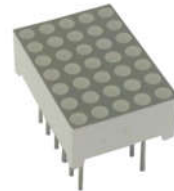
0.70 inch (17.8mm), Yellow Green
Φ1.90mm 5×7 Dot Matrix LED Display



Technical Data Sheet

Features:

- 0.70 inch (17.8mm) Matrix height.
- 5 × 7 dot matrix font.
- Low power consumption.
- Categorized for luminous intensity.
- Choice of colors: Red or Green, etc.
- Choice of face paint colors: Gray or black.
- Design flexibility: Common row anode or common row cathode.
- RoHS Compliant.



Descriptions:

- The KWM-20571AGA/ KWM-20571CGA is a 0.70 inch (17.8mm) height dot matrix display.
- The display provides excellent reliability in bright ambient light.
- The device is made with white dots and gray surface.

Applications:

- Indoor monochromatic display applications, used in variable message signs and static message panels
- Airport, train and bus station display panels
- Electronic message centers---Stock market, department stores, stadiums, banks and cafes
- Safety Signage---Factories and Hospitals

Device Selection Guide:

Part No.	Emitting Color	Polarity
KWM-20571AGA	Yellow Green	Common Row Anode
KWM-20571CGA	Yellow Green	Common Row Cathode

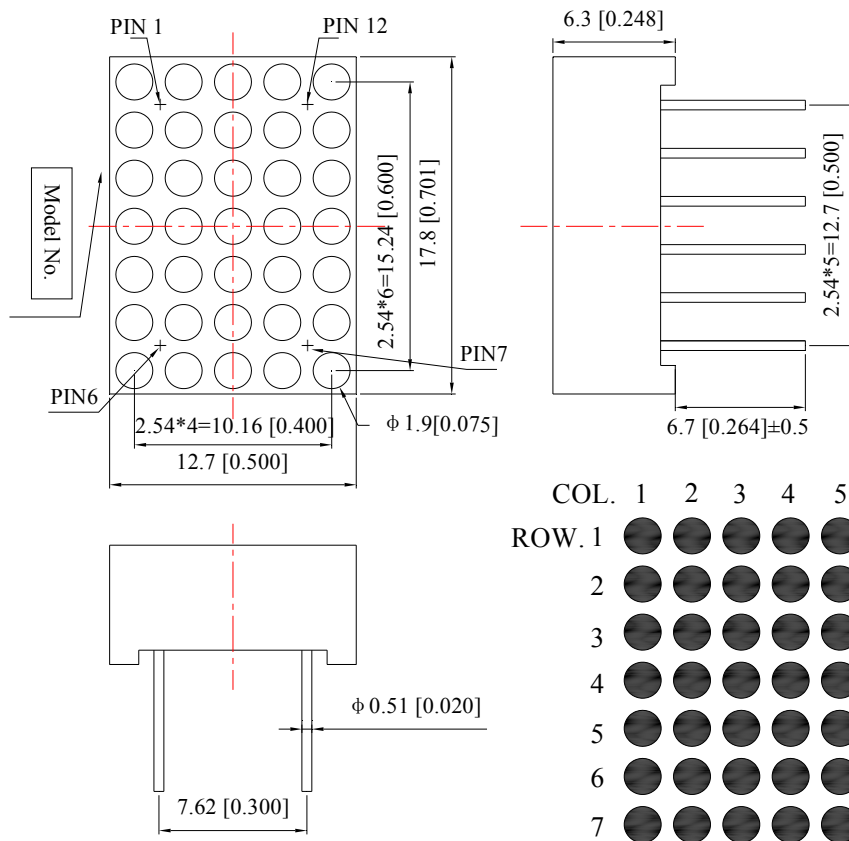
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Package Dimension:



Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is ± 0.25 mm (.010") unless otherwise noted.

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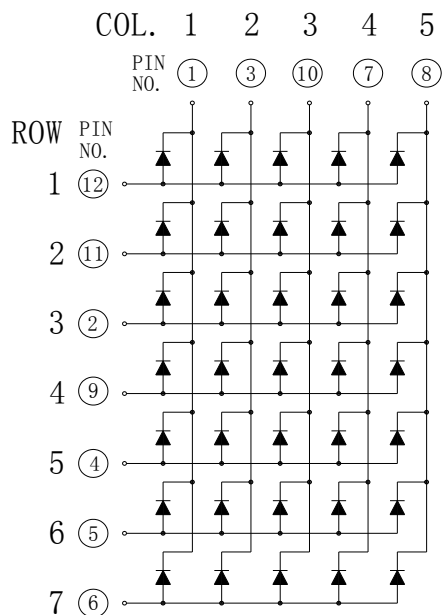
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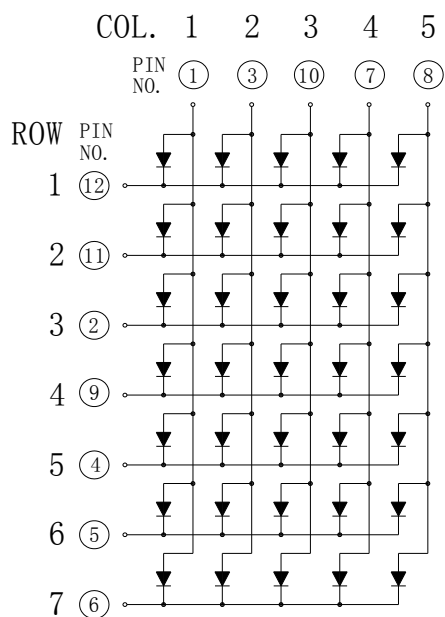
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Internal Circuit Diagram:

Common Row Anode: KWM-20571AGA



Common Row Cathode: KWM-20571CGA



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Absolute Maximum Ratings at Ta=25°C

Parameters	Symbol	Max	Unit
Power Dissipation Per Segment	P_d	48	mW
Peak Forward Current Per Segment (1/10 Duty Cycle, 0.1ms Pulse Width)	I_{FP}	100	mA
Forward Current Per Segment	I_F	20	mA
Reverse Voltage Per Segment	V_R	5	V
Operating Temperature Range	T_{opr}	-40°C to +80°C	
Storage Temperature Range	T_{stg}	-40°C to +85°C	
Soldering Temperature	T_{sld}	260°C for 5 Seconds	

Electrical Optical Characteristics at Ta=25°C

Parameters	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Average Luminous Intensity	I_v	7.5	15.0	---	mcd	IF=10mA (Note a)
		15.0	30.0	---	mcd	IF=20mA (Note a)
Luminous Intensity Matching Ratio	I_{v-m}	---	---	2:1		IF=10mA
Peak Emission Wavelength	λ_p	---	575	---	nm	IF=20mA
Dominant Wavelength	λ_d	---	572	---	nm	IF=20mA (Note b)
Spectral Line Half-Width	$\Delta\lambda$	---	20	---	nm	IF=20mA
Forward Voltage Per Segment	V_F	---	2.0	2.4	V	IF=20mA
Reverse Current Per Segment	I_R	---	---	50	μA	VR=5V

Notes:

- Luminous intensity is measured with a light sensor and filter combination that approximates the CIE eye-response curve.
- The dominant wavelength (λ_d) is derived from the CIE chromaticity diagram and represents the single wavelength which defines the color of the device.

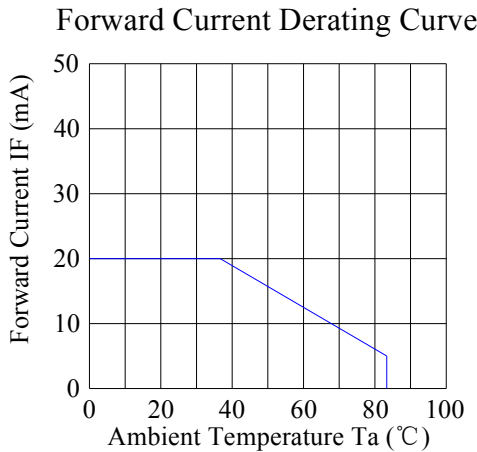
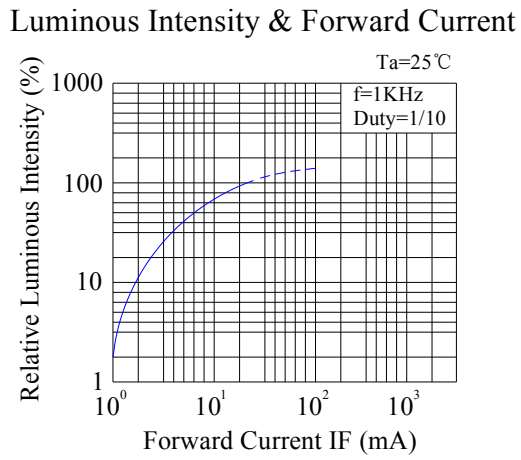
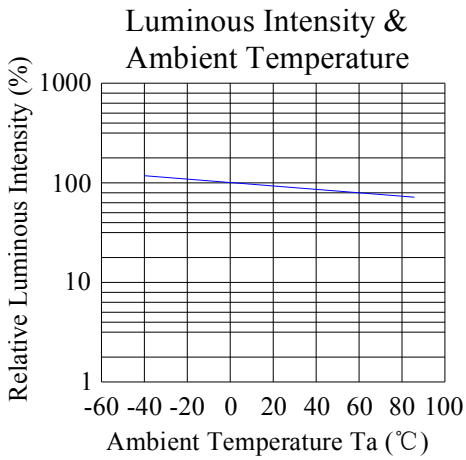
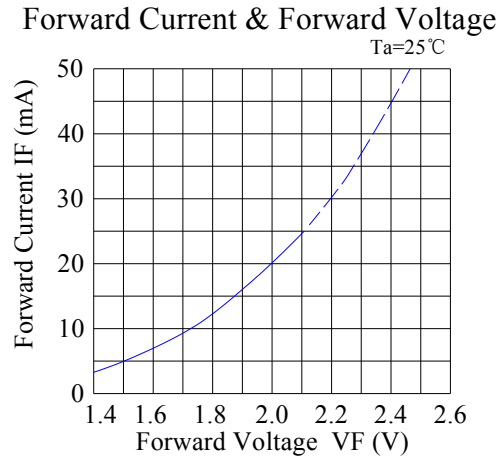
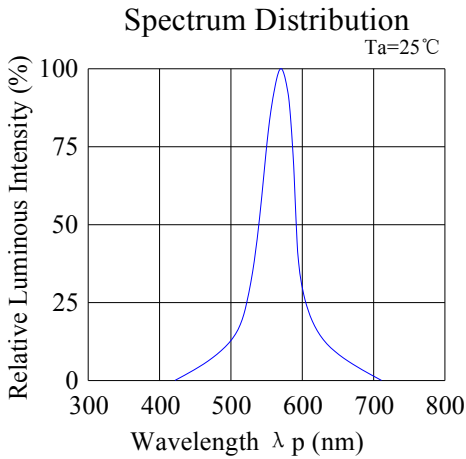
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Typical Electrical / Optical Characteristics Curves (25°C Ambient Temperature Unless Otherwise Noted)



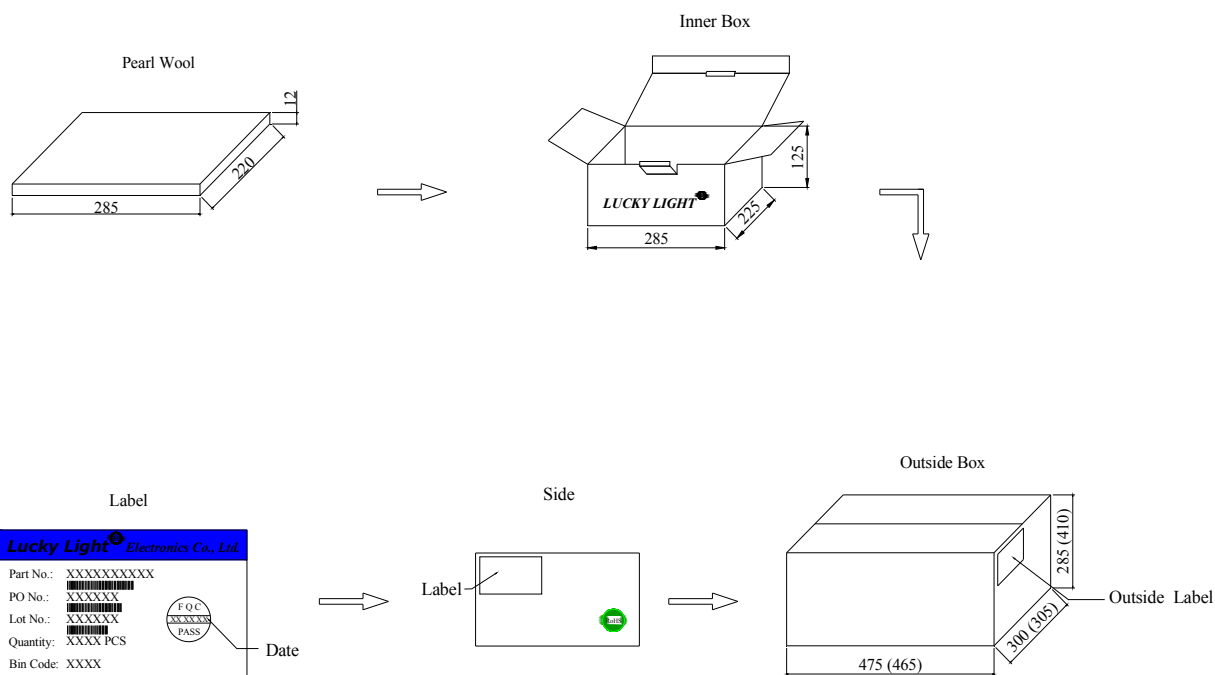
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Packing & Label Specifications:



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Terms and conditions for the usage of this document:

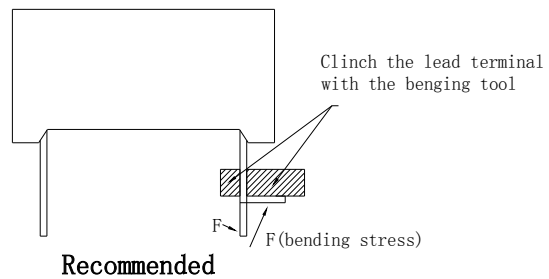
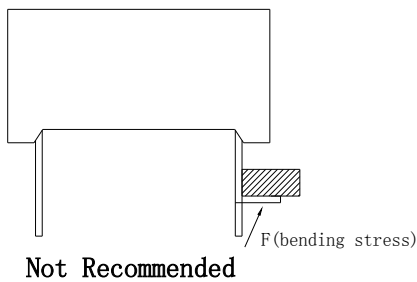
- a. The information included in this document reflects representative usage scenarios and is intended for technical reference only.
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- f. Over-current-proof
Customer must apply resistors for protection, otherwise slight voltage shift will cause big current change (Burn out will happen).
- g. Storage
 1. Before opening the package, the LEDs should be kept at 30°C or less and 80%RH or less.
 2. The LEDs should be used within a year.
 3. After opening the package, the LEDs should be kept at 30°C or less and 60%RH or less.

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Through Hole Display Mounting Method

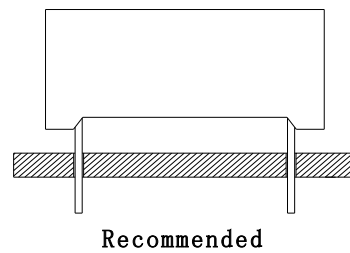
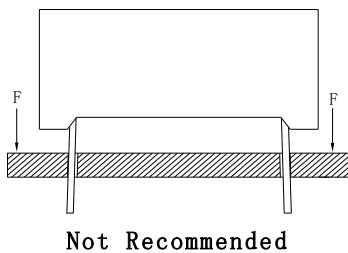
Lead Forming:

1. Do not bend the component leads by hand without proper tools.
2. The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.

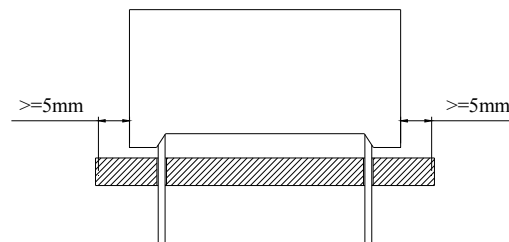
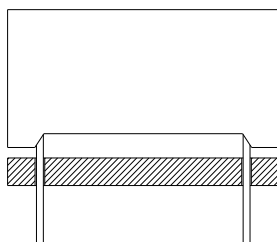


Installation:

1. The installation process should not apply stress to the lead terminals.
2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals.



3. The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering.



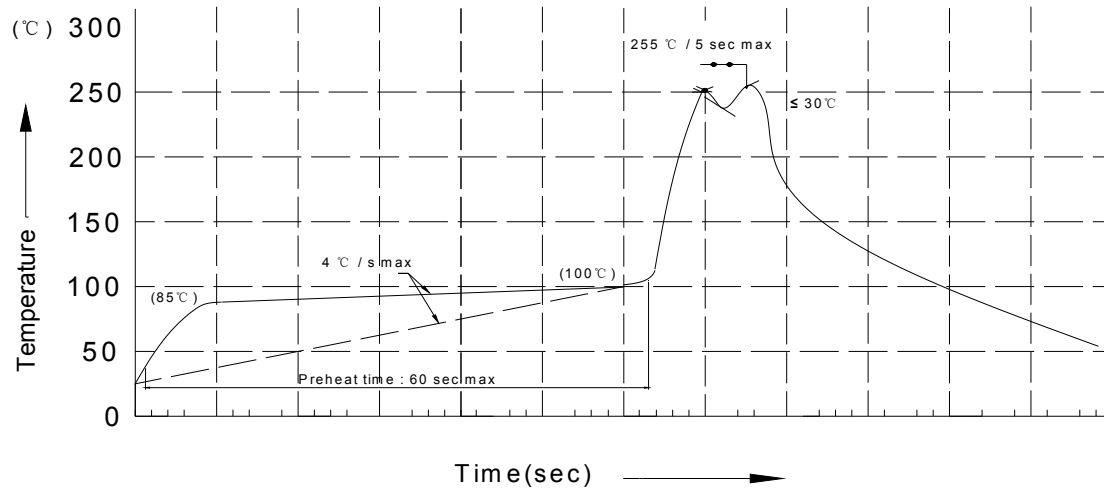
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Recommended Wave Soldering Profiles



Notes:

1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C.
2. Peak wave soldering temperature between 245°C~255°C for 3 sec (5 sec max).
3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
4. Fixtures should not incur stress on the component when mounting and during soldering process.
5. SAC 305 solder alloy is recommended.
6. No more than one wave soldering pass.
7. During wave soldering, the PCB top-surface temperature should be kept below 105°C.

Soldering General Notes:

1. Through-hole displays are incompatible with reflow soldering.
2. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with luckyLight for compatibility.

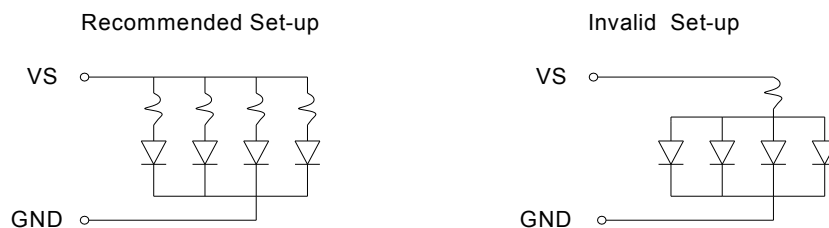
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Cleaning:

1. Mild “no-clean” fluxes are recommended for use in soldering.
2. If cleaning is required, luckylight recommends to wash components with water only.
Do not use harsh organic solvents for cleaning because they may damage the plastic parts.
3. The cleaning process should take place at room temperature and the devices should not be washed for more than one minute.
4. When water is used in the cleaning process, immediately remove excess moisture from the component with forced-air drying afterwards.

Circuit Design Notes:

1. Protective current-limiting resistors may be necessary to operate the LEDs within the specified range.
2. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor.



3. The driving circuit should be designed to protect the LED against reverse voltages and transient voltage spikes when the circuit is powered up or shut down.
4. The safe operating current should be chosen after considering the maximum ambient temperature of the operating environment.
5. Prolonged reverse bias should be avoided, as it could cause metal migration, leading to an increase in leakage current or causing a short circuit.