

## KWM-50571AWB/KWM-50571CWB

2.0 inch (53.1mm), White

Φ5.0mm 5×7 Dot Matrix LED Display

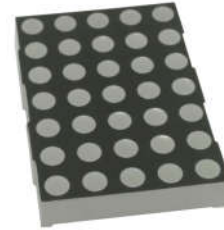
**LuckyLight**

### Technical Data Sheet

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#### Features:

- 2.0 inch (53.1mm) Matrix height
- 5 × 7 dot matrix font
- Low power consumption
- Categorized for luminous intensity
- Choice of colors: Red or Green, etc.
- Choice of face paint colors: Gray or black
- Design flexibility: Common row anode or common row cathode
- RoHS Compliant.



#### Descriptions:

- The KWM-50571AWB/ KWM-50571CWB is a 2.0 inch (53.1mm) height dot matrix display.
- The display provides excellent reliability in bright ambient light.
- The device is made with white dots and black surface.

#### Applications:

- Indoor monochromatic display applications, used in variable message signs and static message panels
- Airport, train and bus station display panels
- Electronic message centers---Stock market, department stores, stadiums, banks and cafes
- Safety Signage---Factories and Hospitals

#### Device Selection Guide:

| Part No.     | Emitting Color | Polarity           |
|--------------|----------------|--------------------|
| KWM-50571AWB | White          | Common Row Anode   |
| KWM-50571CWB | White          | Common Row Cathode |

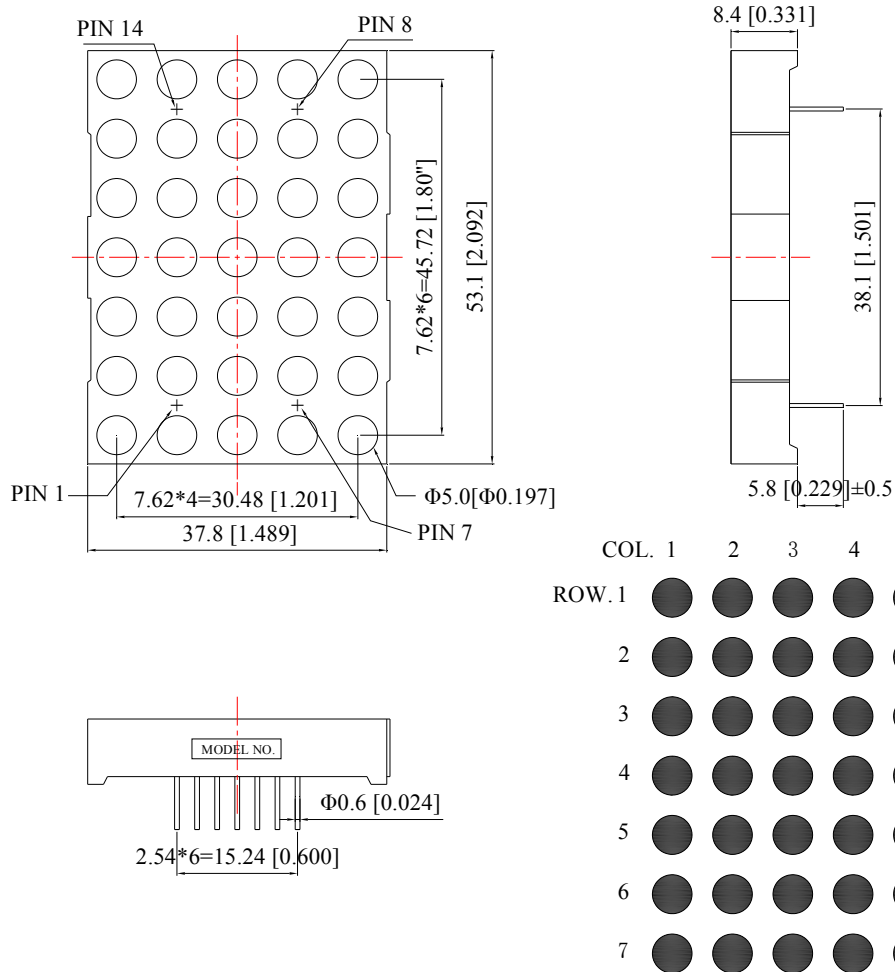
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### Package Dimension:



### Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is ± 0.25 mm (.010") unless otherwise noted.

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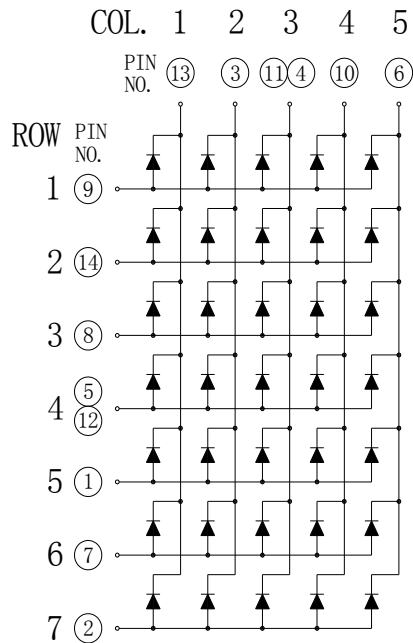
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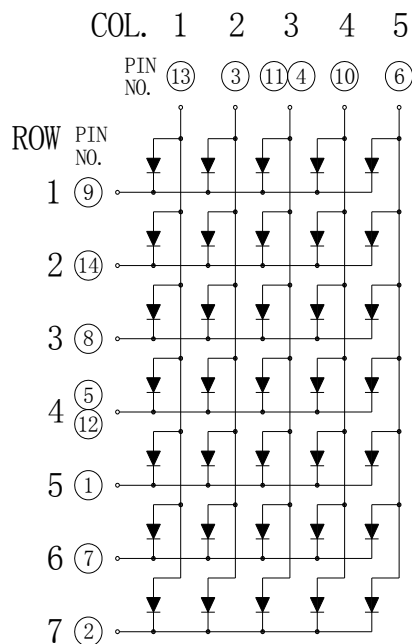
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### Internal Circuit Diagram:

Common Row Anode: KWM-50571AWB



Common Row Cathode: KWM-50571CWB



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### Absolute Maximum Ratings at Ta=25°C

| Parameters   | Symbol    | Max                 | Unit |
|--|-----------|---------------------|------|
| Power Dissipation Per Segment  | $P_d$     | 35                  | mW   |
| Peak Forward Current Per Segment<br>(1/10 Duty Cycle, 0.1ms Pulse Width) | $I_{FP}$  | 50                  | mA   |
| Forward Current Per Segment  | $I_F$     | 10                  | mA   |
| Reverse Voltage Per Segment  | $V_R$     | 5                   | V    |
| Operating Temperature Range  | $T_{opr}$ | -40°C to +80°C      |      |
| Storage Temperature Range  | $T_{stg}$ | -40°C to +85°C      |      |
| Soldering Temperature  | $T_{sld}$ | 260°C for 5 Seconds |      |

### Electrical Optical Characteristics at Ta=25°C

| Parameters                        | Symbol    | Min. | Typ. | Max. | Unit | Test Condition      |
|-----------------------------------|-----------|------|------|------|------|---------------------|
| Average Luminous Intensity        | $I_v$     | 70   | 135  | ---  | mcd  | IF=10mA (Note 1, 2) |
| Luminous Intensity Matching Ratio | $I_{v-m}$ | ---  | ---  | 2:1  |      | IF=10mA             |
| Chromaticity Coordinates          | x         | ---  | 0.30 | ---  |      | IF=10mA (Note 3)    |
|                                   | y         | ---  | 0.31 | ---  |      |                     |
| Forward Voltage Per Segment       | $V_F$     | ---  | 2.9  | 3.10 | V    | IF=10mA             |
| Reverse Current Per Segment       | $I_R$     | ---  | ---  | 50   | μA   | VR=5V               |

#### Notes:

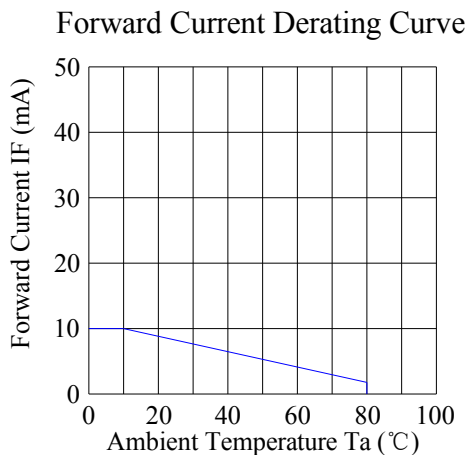
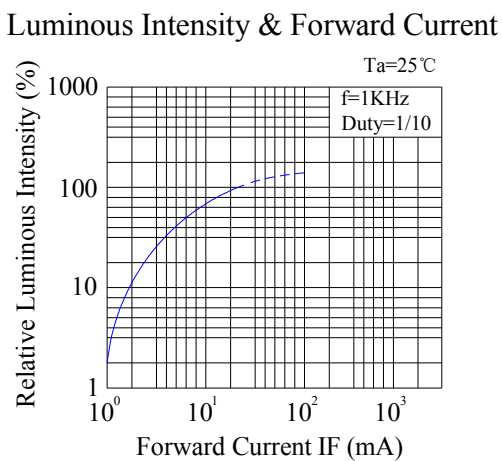
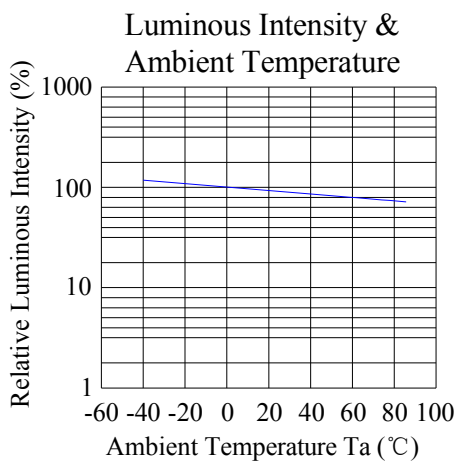
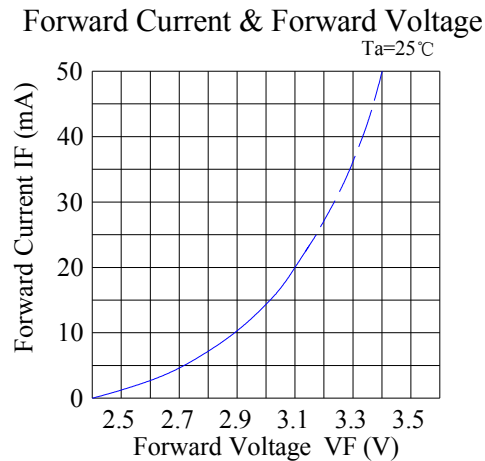
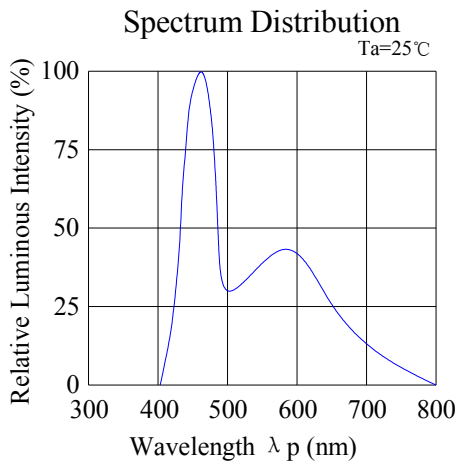
- Luminous Intensity is a average value which is measured one 7-segment. Tolerance of Luminous Intensity: ±10%.
- Excess driving current and / or operating temperature higher than recommended conditions may result in severe light degradation or premature failure.
- The chromaticity coordinates (x, y) is derived from the 1931 CIE chromaticity diagram.

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**Typical Electrical / Optical Characteristics Curves  
(25°C Ambient Temperature Unless Otherwise Noted)**



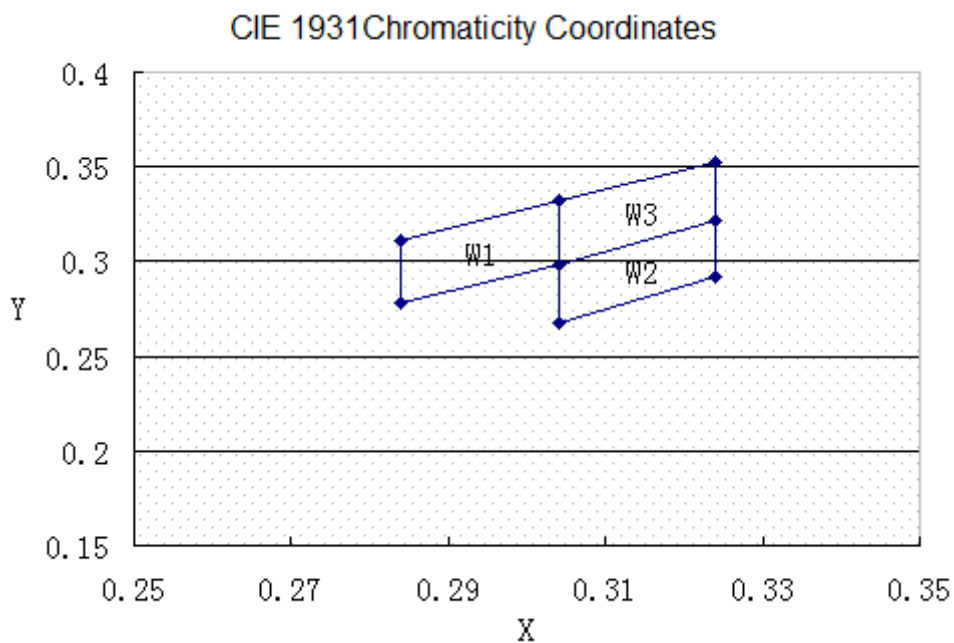
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**Technical Data Sheet****Chromaticity Coordinates Specifications for Bin Rank****Color Bin at IF = 10mA**

| Bin Code | CIE 1931 Chromaticity Coordinates |       |       |       |       |
|----------|-----------------------------------|-------|-------|-------|-------|
|          |                                   | x     | y     | x     | y     |
| W1       | x                                 | 0.284 | 0.284 | 0.304 | 0.304 |
|          | y                                 | 0.278 | 0.311 | 0.332 | 0.298 |
| W2       | x                                 | 0.304 | 0.304 | 0.324 | 0.324 |
|          | y                                 | 0.268 | 0.298 | 0.322 | 0.292 |
| W3       | x                                 | 0.304 | 0.304 | 0.324 | 0.324 |
|          | y                                 | 0.298 | 0.332 | 0.352 | 0.322 |

Tolerance on each Hue (x, y) bin is +/- 0.01.



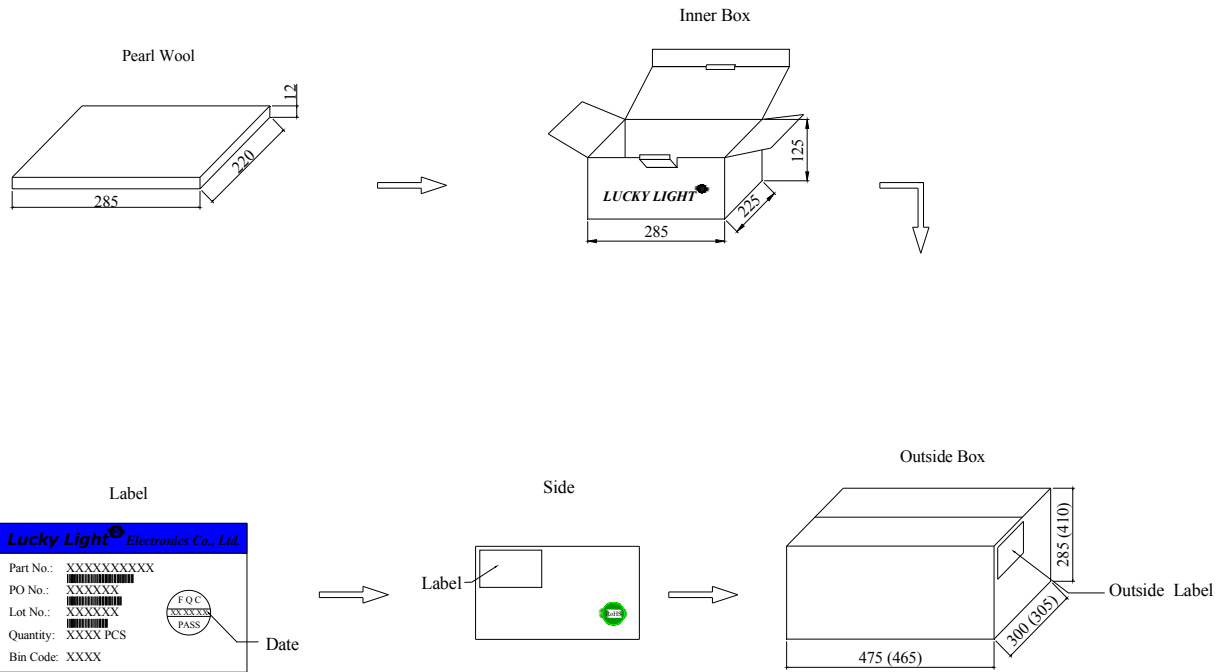
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### Packing & Label Specifications:



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### **Terms and conditions for the usage of this document:**

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- f. Over-current-proof  
Customer must apply resistors for protection, otherwise slight voltage shift will cause big current change (Burn out will happen).

#### **g. Storage**

- 1. Before opening the package, the LEDs should be kept at 30°C or less and 80%RH or less.
- 2. The LEDs should be used within a year.
- 3. After opening the package, the LEDs should be kept at 30°C or less and 60%RH or less.

#### **h. ESD (Electrostatic Discharge)**

Static Electricity or power surge will damage the LED. Suggestions to prevent ESD damage:

Use of a conductive wrist band or anti-electrostatic glove when handling these LEDs.

All devices, equipment, and machinery must be properly grounded.

Work tables, storage racks, etc. should be properly grounded.

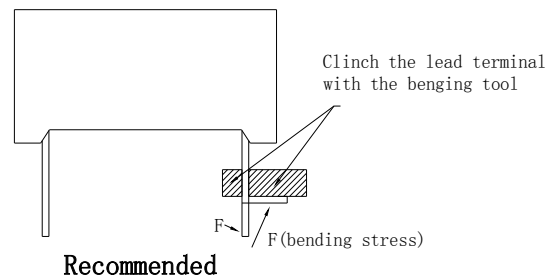
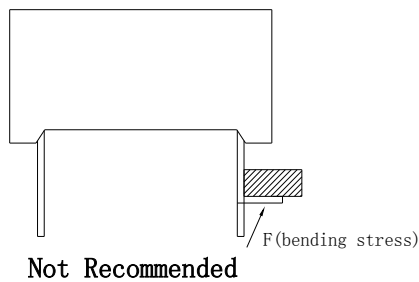


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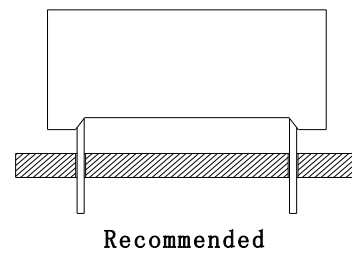
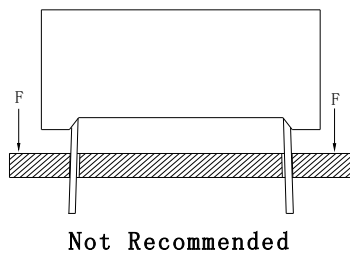
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**Technical Data Sheet****Through Hole Display Mounting Method****Lead Forming:**

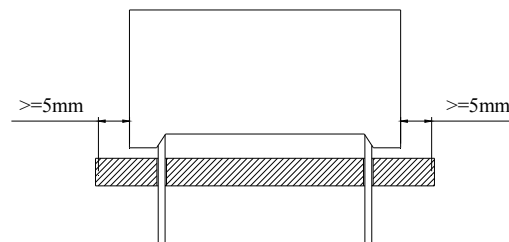
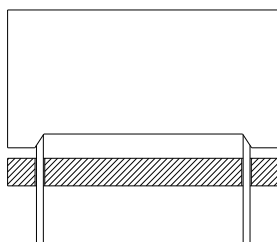
1. Do not bend the component leads by hand without proper tools.
2. The leads should be bent by clinching the upper part of the lead firmly such that the bending force is not exerted on the plastic body.

**Installation:**

1. The installation process should not apply stress to the lead terminals.
2. When inserting for assembly, ensure the terminal pitch matches the substrate board's hole pitch to prevent spreading or pinching the lead terminals.

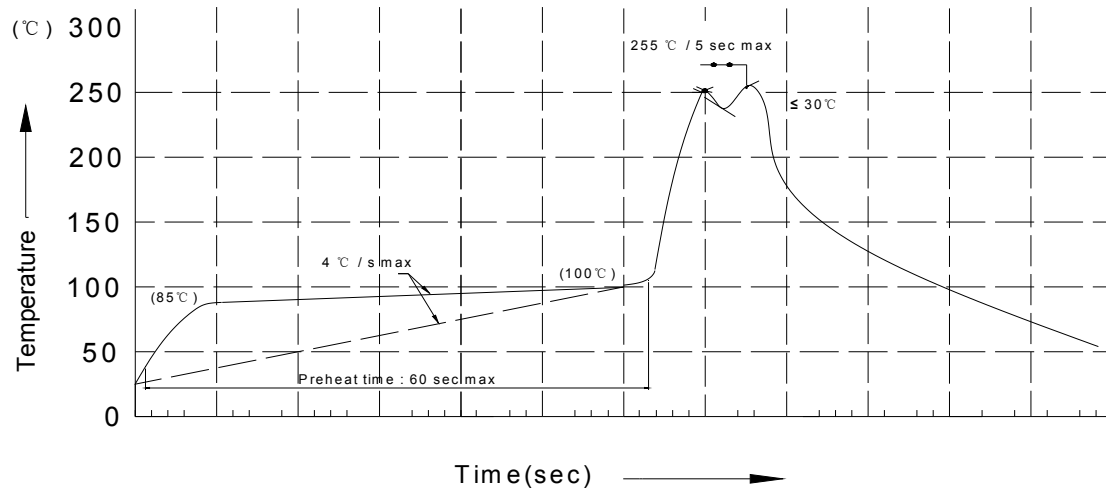


3. The component shall be placed at least 5mm from edge of PCB to avoid damage caused excessive heat during wave soldering.



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**Technical Data Sheet****Recommended Wave Soldering Profiles****Notes:**

1. Recommend pre-heat temperature of 105°C or less (as measured with a thermocouple attached to the LED pins) prior to immersion in the solder wave with a maximum solder bath temperature of 260°C.
2. Peak wave soldering temperature between 245°C~255°C for 3 sec (5 sec max).
3. Do not apply stress to the epoxy resin while the temperature is above 85°C.
4. Fixtures should not incur stress on the component when mounting and during soldering process.
5. SAC 305 solder alloy is recommended.
6. No more than one wave soldering pass.
7. During wave soldering, the PCB top-surface temperature should be kept below 105°C.

**Soldering General Notes:**

1. Through-hole displays are incompatible with reflow soldering.
2. If components will undergo multiple soldering processes, or other processes where the components may be subjected to intense heat, please check with luckyLight for compatibility.

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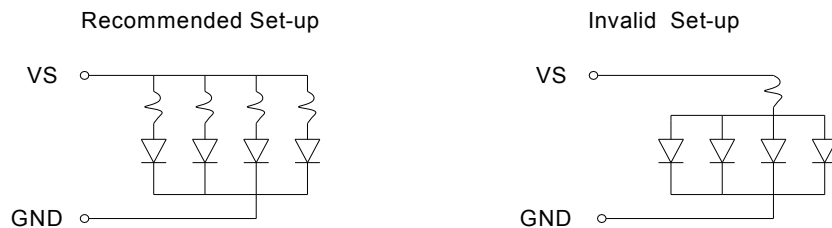
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### Cleaning:

1. Mild “no-clean” fluxes are recommended for use in soldering.
2. If cleaning is required, luckylight recommends to wash components with water only.  
Do not use harsh organic solvents for cleaning because they may damage the plastic parts.
3. The cleaning process should take place at room temperature and the devices should not be washed for more than one minute.
4. When water is used in the cleaning process, immediately remove excess moisture from the component with forced-air drying afterwards.

### Circuit Design Notes:

1. Protective current-limiting resistors may be necessary to operate the LEDs within the specified range.
2. LEDs mounted in parallel should each be placed in series with its own current-limiting resistor.



3. The driving circuit should be designed to protect the LED against reverse voltages and transient voltage spikes when the circuit is powered up or shut down.
4. The safe operating current should be chosen after considering the maximum ambient temperature of the operating environment.
5. Prolonged reverse bias should be avoided, as it could cause metal migration, leading to an increase in leakage current or causing a short circuit.